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| APPLICATION NO. | FILI | NG DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|-------------------|---------|----------------------|-------------------------|------------------|
| 10/005,627 | 12/ | 04/2001 | Nai-Shung Chang | JCLA6879 | 8225 |
| 23900 | 7590 ' 08/04/2005 | | | EXAMINER | |
| J C PATENTS, INC. 4 VENTURE, SUITE 250 | | | | CHEN, TSE W | |
| IRVINE, CA 92618 | | | | ART UNIT | PAPER NUMBER |
| | | | • | 2116 | |
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Please find below and/or attached an Office communication concerning this application or proceeding.

| Application No. | Applicant(s) | | |
|-----------------|--------------|--|--|
| 10/005,627 | CHANG ET AL. | | |
| Examiner | Art Unit | | |
| Tse Chen | 2116 | | |

Advisory Action Before the Filing of an Appeal Brief --The MAILING DATE of this communication appears on the cover sheet with the correspondence address --THE REPLY FILED 27 July 2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. 1. Mar The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods: a) The period for reply expires <u>3</u> months from the mailing date of the final rejection. b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL 2. The Notice of Appeal was filed on . A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a). 3. 🔀 The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will <u>not</u> be entered because (a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below); (b) They raise the issue of new matter (see NOTE below); (c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or (d) They present additional claims without canceling a corresponding number of finally rejected claims. NOTE: See Continuation Sheet. (See 37 CFR 1.116 and 41.33(a)). 4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324). 5. Applicant's reply has overcome the following rejection(s): 6. Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s). 7. X For purposes of appeal, the proposed amendment(s): a) X will not be entered, or b) X will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended. The status of the claim(s) is (or will be) as follows: Claim(s) allowed: Claim(s) objected to: Claim(s) rejected: 1-6 and 8-16. Claim(s) withdrawn from consideration: AFFIDAVIT OR OTHER EVIDENCE 8. 🗌 The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e). 9. 🔲 The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will <u>not</u> be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1). 10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached. REQUEST FOR RECONSIDERATION/OTHER 11.

The request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet. 12. Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s). 13. 🔲 Other: __ É H. BROWNE SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2100

Continuation of 3. NOTE: Applicant's amendment stipulating that "the control signal opens the plurality of switches to cut off the coupling between the memory module slot and the voltage source" would require further consideration and/or search.

Continuation of 11. does NOT place the application in condition for allowance because: Applicant's arguments filed July 27, 2005 have been fully considered but they are not persuasive. Applicant alleges that "it is obvious that a skilled person would not have the motivation to combine Fan and Taguchi ... because Fan did not disclose any suggestion or motivation for power conservation". Examiner recognizes that obviousness can only be established by combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either in the references themselves or in the knowledge generally available to one of ordinary skill in the art. See In re Fine, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988) and In re Jones, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992). In this case, Examiner submits that the explicit motivation to reduce power consumption, a desirable aspect known to one of ordinary skill in the art, was found in Taguchi [col.1, l.65- col.2, l.11; col.2, l.50 - col.3, I.2; col.5, II.33-48]. Applicant alleges that in Fan, "only a single termination resistor is disclosed". Examiner disagrees and submits that Fan does disclose a plurality of termination resistors as discussed in the rejection. Furthermore, Applicant did not dispute the fact that a single resistor may embody a plurality of resistors according to Kirchoff's Law. Applicant alleges that Taguchi "does not clearly disclose the connection between the switch and the termination resistor". Examiner disagrees and submits that Taguchi does disclose the connection between the switch and the termination resistor as discussed in the rejection. Applicant alleges that neither Fan nor Taguchi "discloses how the control signal commands the switch to cut off the connection between the termination resistor and the voltage when the memory module is not inserted in the memory module slot... it is advised that a power saving mode is not always the same as an empty memory module slot mode". Examiner submits that the rejection is valid since the limitation specifically states that the control signal commands the switch to cut off the connection between the termination resistor and the voltage "when the motherboard enters a power saving mode OR when the memory module is not inserted in the memory module slot". As demonstrated above, Applicant's arguments are not persuasive and the rejections are respectfully maintained.